

# EDA for Hardware Security in IC Design -2026

- 1 Hardware trojan (16 pages)
- 2-1 Review of ATPG (14 pages)
- 2-2 Trojan detection (28 pages)
- 3 IC camouflaging (13pages)
- 4 Split manufacturing (19 pages)
- 5-1 Logic locking (37 pages)
- 5-2 SAT attack (40 pages)
- 5-3 Anti-SAT logic locking by point function (30 pages)
- 5-4 Anti-SAT logic locking by adding cycle (28 pages)
- 6 Sequential circuit (25 pages)