

# CS1356 Introduction to Information Engineering

## Homework 2

1. Convert each of the following 5-bit two's complement representation to its equivalent base ten representation

a. 01111      b. 10011      c. 01101  
d. 10000      e. 10111

2. Convert each of the following base ten representation to its equivalent 5-bit two's complement representation

a. 12              b. -12              c. -1  
d. 0                e. 8

3. Perform each of the following additions assuming the bit pattern represent values in two's complement notation. Identify the cases that will cause overflow.

a. 
$$\begin{array}{r} 00101 \\ +01000 \\ \hline \end{array}$$
      b. 
$$\begin{array}{r} 11111 \\ +00001 \\ \hline \end{array}$$
      c. 
$$\begin{array}{r} 01111 \\ +00001 \\ \hline \end{array}$$
  
d. 
$$\begin{array}{r} 10111 \\ +11010 \\ \hline \end{array}$$
      e. 
$$\begin{array}{r} 00111 \\ +00111 \\ \hline \end{array}$$
      f. 
$$\begin{array}{r} 00111 \\ +01100 \\ \hline \end{array}$$
  
g. 
$$\begin{array}{r} 11111 \\ +11111 \\ \hline \end{array}$$
      h. 
$$\begin{array}{r} 01010 \\ +00011 \\ \hline \end{array}$$
      i. 
$$\begin{array}{r} 01000 \\ +01000 \\ \hline \end{array}$$
  
j. 
$$\begin{array}{r} 01010 \\ +10101 \\ \hline \end{array}$$

4. Solve each of the following problems by translating the values into two's complement notation (5-bit), converting any subtraction problem to an equivalent addition problem, and performing that addition.

a. 
$$\begin{array}{r} 7 \\ +1 \\ \hline \end{array}$$
      b. 
$$\begin{array}{r} 7 \\ -1 \\ \hline \end{array}$$
      c. 
$$\begin{array}{r} 12 \\ -4 \\ \hline \end{array}$$
  
d. 
$$\begin{array}{r} 8 \\ -7 \\ \hline \end{array}$$
      e. 
$$\begin{array}{r} 12 \\ -4 \\ \hline \end{array}$$
      f. 
$$\begin{array}{r} 5 \\ -11 \\ \hline \end{array}$$